

# HITK0204MP

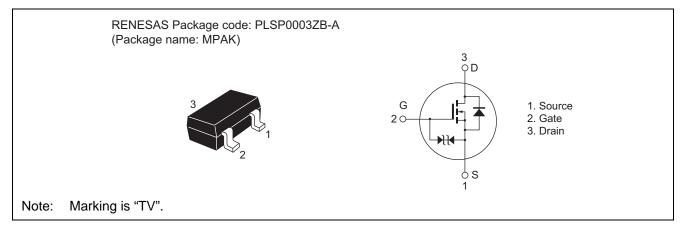
20V, 2.3A, 130mΩmax. Silicon N Channel MOS FET Power Switching

R07DS0482EJ0200 Rev.2.00 May 09, 2013

#### Features

- Low on-resistance
- $R_{DS(on)} = 100 \text{ m}\Omega \text{ typ} (V_{GS} = 4.5 \text{ V}, I_D = 1.2 \text{ A})$
- Low drive current
- High speed switching
- 2.5 V gate drive

### Outline



## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
ltem	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	20	V
Gate to source voltage	V <sub>GSS</sub>	±12	V
Drain current	Ι <sub>D</sub>	2.3	А
Drain peak current	I <sub>D(pulse)</sub> Note1	8.0	А
Body - drain diode reverse drain current	I <sub>DR</sub>	2.3	А
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \leq 10~\mu s,~duty~cycle \leq 1\%$ 

2. When using the glass epoxy board (FR-4: 40 x 40 x 1 mm)



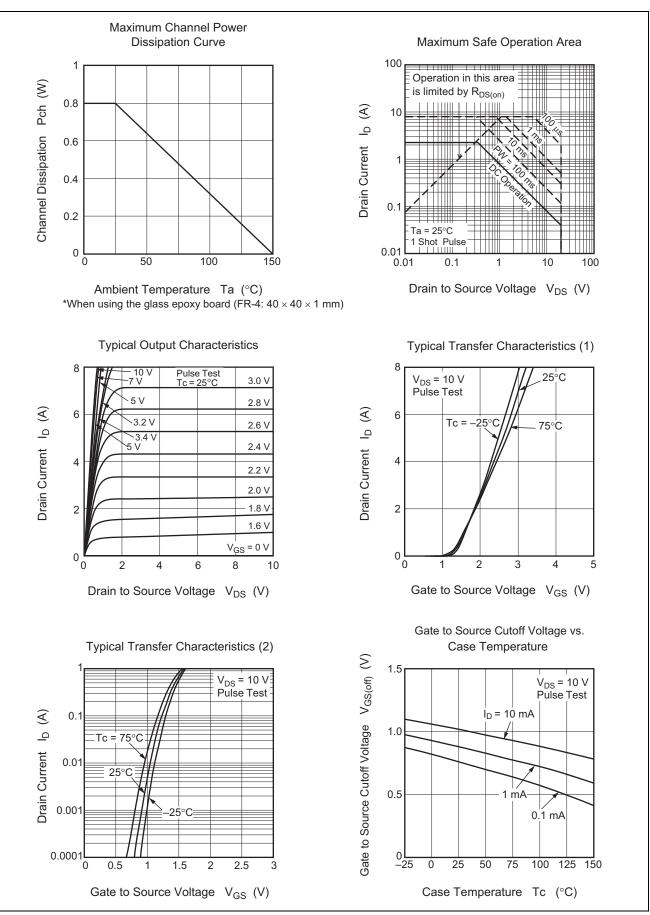
# **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$	
ltem	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	20	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±12	—	—	V	$I_G=~\pm 100~\mu A,~V_{DS}=0$	
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μA	$V_{GS} = \pm 10$ V, $V_{DS} = 0$	
Drain to source leak current	I <sub>DSS</sub>	_	_	1	μA	$V_{DS} = 20 V, V_{GS} = 0$	
Gate to source cutoff voltage	V <sub>GS(off)</sub>	0.4	—	1.4	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	
Drain to source on state resistance	R <sub>DS(on)</sub>		100	130	mΩ	$I_D = 1.2 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note3}}$	
	R <sub>DS(on)</sub>		146	204	mΩ	$I_D = 1.2 \text{ A}, V_{GS} = 2.5 \text{ V}^{\text{Note3}}$	
Forward transfer admittance	y <sub>fs</sub>	1.5	3.0		S	$I_D = 1.2 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note3}}$	
Input capacitance	Ciss		127		pF	V <sub>DS</sub> = 10 V	
Output capacitance	Coss	_	33	_	pF	V <sub>GS</sub> = 0 f = 1 MHz	
Reverse transfer capacitance	Crss	_	14	—	pF		
Turn - on delay time	t <sub>d(on)</sub>	_	11	—	ns	I <sub>D</sub> = 1.2 A	
Rise time	tr	_	28	—	ns	$V_{GS} = 10 V$ $R_L = 8.3 \Omega$ $Rg = 4.7 \Omega$	
Turn - off delay time	t <sub>d(off)</sub>	_	24	—	ns		
Fall time	t <sub>f</sub>	_	7	—	ns		
Total gate charge	Qg	_	1.5	—	nC	V <sub>DD</sub> = 10 V	
Gate to source charge	Qgs		0.3		nC	$V_{GS} = 5 V$	
Gate to drain charge	Qgd		0.4	—	nC	I <sub>D</sub> = 2.3 A	
Body - drain diode forward voltage	V <sub>DF</sub>	_	0.85	1.1	V	$I_F = 2.3 \text{ A}, V_{GS} = 0^{Note3}$	

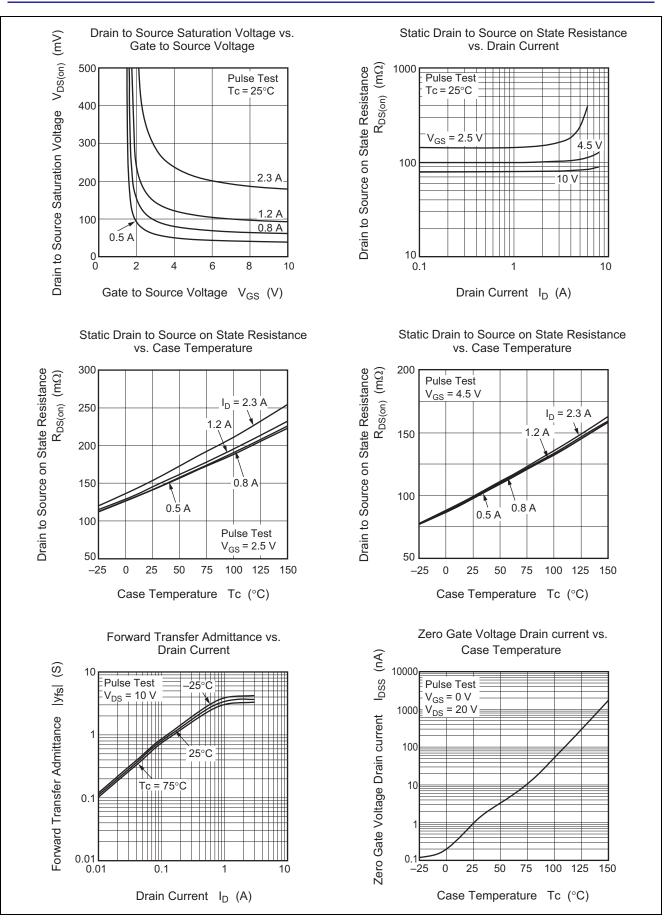
Notes: 3. Pulse test

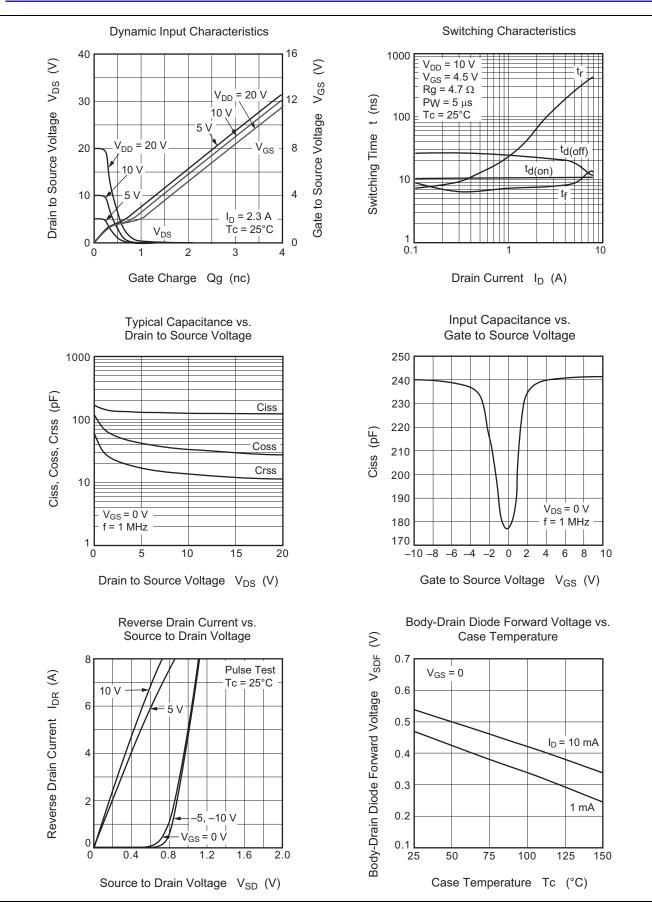


#### **Main Characteristics**

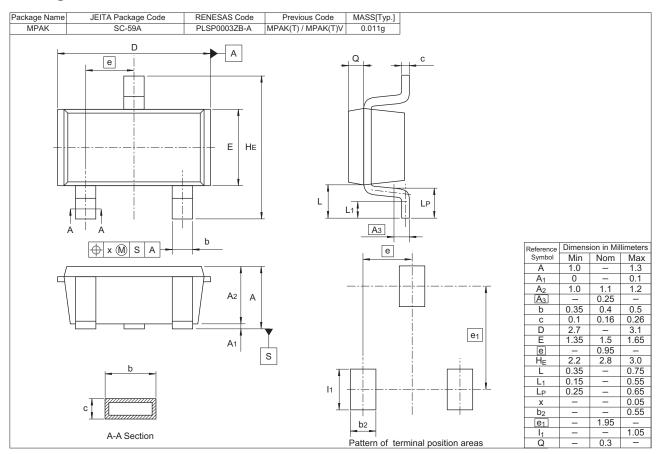








## **Package Dimensions**



## **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
HITK0204MPTL-HQ	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

Note: This product is designed for consumer use and not for automotive.



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